

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 07/869,851	GROUP PART UNIT 2306	ATTACHMENT TO PAPER NUMBER 36
NOTICE OF REFERENCES CITED				APPLICANT(S) Itch et al.		

U.S. PATENT DOCUMENTS							
*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
*A	2697201	12/1954	Hamder	323	229		
*B	3386030	5/1968	Kann	323	303		
*C	3523195	8/1970	Thomas et al.	235	197		
*D	3553487	1/1971	Freeborn	307	229		
*E	3742338	6/1973	Sugano et al.	307	297		
*F	4368524	1/1983	Nakamura et al.	365	226		
*G	4409677	10/1983	Takemae et al.	365	206		
*H	4455628	6/1984	Ozaki et al.	365	226		
*I	4495622	1/1985	Charrau	371	20		
*J	4585955	4/1986	Uchida	307	297		
K							

FOREIGN PATENT DOCUMENTS									
*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG. PP. SPEC.		
*L	55-47414	4/1980	Japan	Yoshioka	324	132			
*M	59-191935	10/1984	Japan	Itou	324	158R			
N									
O									
P									
Q									

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
*	R Love, R, et al, "Width-to-Length Ratio Design Program for Interacting Static FET Circuits", IBM Technical Disclosure Bulletin, Vol. 16, No. 11, April 1974, pp 3671-3673
	S
	T
	U

EXAMINER <i>Stephen Baker</i>	DATE 12/6/95	
----------------------------------	------------------------	--

* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)